

**Amendments to the Claims:**

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) A method comprising:

~~initiating an operation in a second portion of a system if a value indicates that a first portion of the system is in a trusted state.~~  
receiving, with a first processor, data for use in an operation in a second processor, the second processor being a wireless communications processor;

verifying, with the first processor, a credibility of the data for the second processor;

placing the first and second processors in a trusted state;

exiting the trusted state if the credibility of the data fails; and

initiating the operation with the second processor while the trusted state has not been exited.

2. (Original) The method of claim 1, wherein the operation comprises an information update.

3. (Currently Amended) The method of claim 2, further comprising:

setting a register containing ~~the a~~ value using the first ~~portion before~~processor when  
exiting the trusted state.

4. (Currently Amended) The method of claim 3, further comprising reading the value using the second ~~portion.~~processor.

5. (Currently Amended) The method of ~~claim 1,~~claim 3, further comprising ~~not performing~~preventing an execution of the operation if the value is not indicative of the trusted state.

6. (Currently Amended) The method of ~~claim 1,~~claim 3, further comprising initiating remediation if at least the value is not indicative of the trusted state.

7. (Currently Amended) The method of claim 2, further comprising receiving the information update via an air interface with the second ~~portion-processor~~ and providing the information update to the first ~~portion-processor~~.

8. (Canceled)

9. (Currently Amended) A method comprising:  
maintaining a hardware asset with an applications processor of a system to indicate to another hardware component of the system a trust state of the applications processor.~~a first subsystem of a system.~~

10. (Currently Amended) The method of claim 9, further comprising accessing the hardware asset using ~~a second subsystem~~ the other hardware component of the system.

11. (Original) The method of claim 9, wherein the system comprises a wireless device.

12. (Currently Amended) The method of claim 10, further comprising updating digital content in the ~~second subsystem~~ other hardware component if the hardware asset indicates the trust state is valid.

13. (Currently Amended) The method of claim 10, further comprising preventing updating the ~~second subsystem~~ other hardware component if the hardware asset does not indicate the trust state is valid.

14. (Currently Amended) The method of claim 13, further comprising performing a remediation measure using the ~~second subsystem~~ other hardware component if the trust state is not valid.

15. (Currently Amended) The method of claim 13, further comprising providing an indication to the ~~first subsystem~~ applications processor if an update was attempted when the trust state was not valid.

16. (Currently Amended) The method of claim 9, further comprising setting the hardware asset via the ~~first subsystem before~~ applications processor when exiting a trusted state, wherein the hardware asset comprises a one-way register.

17. (Currently Amended) The method of claim 10, further comprising

determining if an update is trusted in the ~~first subsystem~~applications processor and transferring the update to the ~~second subsystem~~other hardware component if the hardware asset indicates the trust state is valid.

18. (Currently Amended) An apparatus comprising:

a hardware ~~asset~~asset, maintained by an applications processor, to indicate a trust state of ~~an applications~~an application processor portion of the ~~apparatus~~apparatus to a communications processor.

19. (Currently Amended) The apparatus of claim 18, wherein the hardware asset is accessible by ~~a communications portion~~the communications processor of a wireless device.

20. (Currently Amended) The apparatus of claim 19, wherein the communications ~~portion~~processor cannot modify a value of the hardware asset.

21. (Currently Amended) The apparatus of claim 18, wherein the hardware asset is coupled to receive a program signal if the trust state of the applications processor portion is not valid.

22. (Original) The apparatus of claim 18, wherein the hardware asset is coupled to receive a reset signal to initiate a trusted state.

23. (Original) The apparatus of claim 18, wherein the hardware asset comprises a one-

way register.

24. (Currently Amended) A system comprising:

a hardware asset to indicate a trust state of an applications processor portion of the system  
to a communications processor portion of the system; and  
a wireless interface coupled to the hardware asset.

25. (Currently Amended) The system of claim 24, wherein the hardware asset is  
accessible by ~~a communication~~ the communications processor portion of the system, wherein the  
system comprises a wireless device.

26. (Original) The system of claim 24, wherein the hardware asset comprises a one-way  
register.

27. (Original) The system of claim 24, wherein the wireless interface comprises an  
antenna.

28. (Currently Amended) An article including a machine-accessible storage medium  
containing instructions that if executed enable a system to:

control a hardware asset of the system with an applications processor to indicate a trust  
state of ~~a first~~ an applications processor portion of the system to another hardware portion of the  
system.

29. (Currently Amended) The article of claim 28, further comprising instructions ~~that~~ that, if executed ~~executed~~, enable the system to update ~~a second~~ the other hardware portion of the system if the hardware asset indicates the trust state is valid.

30. (Currently Amended) The article of claim 28, further comprising instructions that if executed enable the system to prevent or discard an update to ~~a second~~ the other hardware portion of the system if the hardware asset indicates the trust state is not valid.

31. (Currently Amended) The article of claim 30, further comprising instructions that if executed enable the ~~second~~ other hardware portion to initiate a remediation operation if the hardware asset indicates the trust state is not valid.

32. (Currently Amended) The article of claim 28, further comprising instructions ~~that~~ that, if executed ~~executed~~, enable the system to perform a secure operation in ~~a second~~ the other hardware portion of the system if the hardware asset indicates the trust state is valid.

33. (Currently Amended) The article of claim 28, further comprising instructions ~~that~~ that, if executed ~~executed~~, enable the ~~first~~ applications processor portion to vector into a trusted state before initiating a transfer operation to ~~a second~~ the other hardware portion of the system.

34. (Currently Amended) A method comprising:

setting a value to indicate a trust state of an applications processor portion of a system;

accessing a ~~value~~the value with a ~~second communications processor~~ portion of a system;  
~~and the value indicative of a trust state of a first portion of the system.~~

determining the trust state of the applications processor portion based on the value.

35. (Currently Amended) The method of claim 34, further comprising initiating an operation in the ~~second communications processor~~ portion if the value is indicative of the trust state.

36. (Original) The method of claim 35, wherein the operation comprises an information update.

37. (Currently Amended) The method of claim 34, wherein the ~~first applications processor~~ portion comprises an applications portion of a wireless device and the ~~second communications processor~~ portion comprises a communications portion of the wireless device.